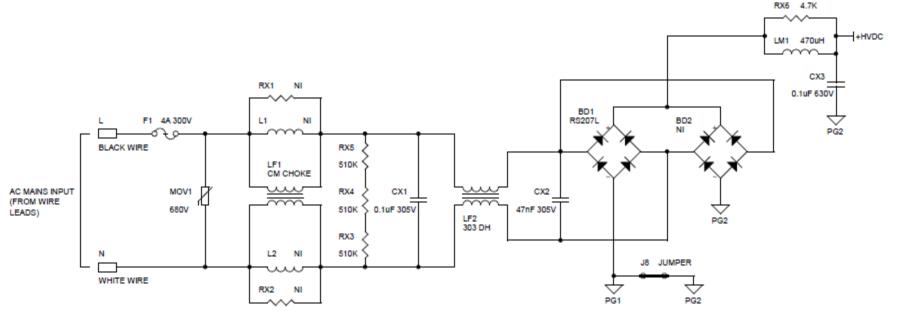
EXHIBIT 18



POWER INPUT AND CONDITIONING

